

SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)

Seshadri Rao Knowledge Village, Gudlavalleru

M.Tech I Semester (R25) Second Mid Term Examinations

TIME TABLE

Timings: 10.00 a.m. to 12.00 Noon

16th February 2026

Date	Structural Engineering	Power Electronics & Electric Drives	VLSI Design & Embedded Systems	CSE (Artificial Intelligence & Machine Learning)
23.02.2026 (Monday)	Theory of Elasticity (D2518701)	Electrical Machine Modeling and Analysis (D2515401)	Advanced Digital Systems Design (D2517701)	Mathematics for Machine Learning (D251AI01)
24.02.2026 (Tuesday)	Structural Dynamics (D2518702)	Power Electronic Converters (D2515402)	Embedded Hardware Platforms and Programming (D2517702)	Artificial Intelligence (D251AI02)
25.02.2026 (Wednesday)	Matrix Analysis of Structures (D2518703)	Electric Vehicles (D2515403)	FPGA Design (D2517703)	Data Science Applications (D251AI03)
26.02.2026 (Thursday)	Analytical & Numerical Methods for Structural Engineering (D25187A1)	Industrial Control Electronics (D25154A2)	VLSI System Design (D25177A2)	Advanced Data Mining (D251AIA0)
27.02.2026 (Friday)	Repair and Rehabilitation of Structures (D25187B1)	Renewable Energy Technology (D25154B1)	System on Chip Design (D25177B0)	Recommender Systems (D251AIB3)

Note: Seating arrangement will be notified before the commencement of the examination by the respective departments.

M. Kusana
Vice Principal
(Academics)

Copy to: HODs – CE/EEE/ECE/CSE/BS&H/CoE

Tables of Principal/Vice-Principal (Academics)/Vice-Principal (Administration)